

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,062,245 B2
APPLICATION NO. : 09/739817
DATED : June 13, 2006
INVENTOR(S) : K. Miyano et al.

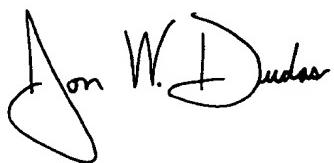
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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

- At column 1, line 37 of the printed patent, "Is" should be --is--.
At column 1, line 37 of the printed patent, "0 or 0" should be -- "1 or 0"--.
At column 5, line 23 of the printed patent, "Is" should be --is--.
At column 3, line 16 of the printed patent, "comparing circuit 117" should be --comparing circuit 116--.
At column 3, line 54 of the printed patent, "polarization antenna 106" should be --polarization antenna 107--.
At column 6, line 16 of the printed patent, "multiplier 303" should be --multiplier 301--.
At column 6, line 17 of the printed patent, "transmission signal 303" should be --transmission signal 302--.
At column 6, line 20 of the printed patent, "transmission signal 303" should be --transmission signal 302--.
At column 6, line 33 of the printed patent, "D-flip flop" should be --D-flip flop 401--.

Signed and Sealed this

Twelfth Day of December, 2006



JON W. DUDAS
Director of the United States Patent and Trademark Office